ATRENTA INC.
CORPORATE OVERVIEW

Atrenta's Market

Atrenta is part of the ~$5B electronic design automation (EDA) market. This market is composed of approximately 250 companies that supply software, specialized hardware and services to assist in the design of complex semiconductors, often called system-on-chip (SoC) devices.

The primary customers in this market are vertically integrated electronics companies, fabless semiconductor companies and end product developers who use SoCs to differentiate their products.

Product Strategy

Atrenta provides software tools and services to assist in the design and functional verification of SoCs during the early stages of the design process. In this context, early in the process refers to the architectural and functional definition stages, when the design is expressed at the register transfer level (RTL) of abstraction.

Atrenta's products allow designers to know as early as possible if their design will meet its requirements for things like performance, power and cost and ensure that the verification of the design is complete.

The software will also identify problems in the design and repair them at the earliest possible time. We broadly refer to this process as SoC Realization. Companies who have adopted this approach include many of the largest and most successful semiconductor and consumer electronics companies in the world.

Value Proposition

As a general rule, the later a problem is found in an SoC design, the more costly it is to fix. By identifying problems early, Atrenta is able to significantly reduce the cost of SoC design and at the same time dramatically improve schedule predictability.

A good analogy to understand the benefits of this process is to consider a complex construction project. Before ground is broken for the project, a detailed set of blueprints is developed. Errors found during this phase of the design are easily repaired by amending the blueprints.

Once construction begins, the time and cost associated with repairing an error become much larger, especially if the error involves a portion of the project that has already been fabricated incorrectly. These same parallels exist in SoC design and Atrenta's products focus on the "blueprint stage" of the process, where errors are fixed more easily.

Company Status

Atrenta is the largest private EDA company in the industry. The Company has enjoyed eight consecutive years of revenue growth. We believe the Company is becoming a broad line supplier that is on the cusp of achieving profitability.

Atrenta maintains a direct sales and applications engineering team, supplemented by a small number of distributors. The Company also maintains strong research and development activities in San Jose, US; Noida, India; Grenoble, France; Colombo, Sri Lanka and Shanghai, China.

ATRENTA INC.
CORPORATE FACT SHEET

Atrenta's SpyGlass® Predictive Analysis software platform significantly improves design efficiency for the world's leading semiconductor and consumer electronics companies.

Founded
June 2001

Funding
Atrenta Inc. is privately held and venture funded. Major investors include Venrock Associates, Investcorp Technology Ventures, Investor Growth Capital and Partner Ventures

Executive Team

Ajoy Bose, PhD	Chairman, President and CEO
Bert Clement	Chief Financial Officer
Mike Fazeli	Vice President of Strategic Development
Mike Gianfagna	Vice President of Corporate Marketing
Sushil Gupta	Vice President and Managing Director, India
Mo Movahed	Vice President of Engineering
Bernard Murphy, PhD	Chief Technology Officer
Piyush Sancheti	Vice President of Product Marketing
Charlie Schadewitz	Vice President of Worldwide Sales
Ravi Varadarajan	Atrenta Fellow
Yunshan Zhu, PhD	Vice President of New Technologies

Patents
21 granted and 13 patents pending

Products
Atrenta's SpyGlass, GenSys® and BugScope™ product families of SoC Realization tools enhance the entire system development cycle -- from architectural planning to physical implementation. The family includes:

- SpyGlass (Static and structural RTL checks)
- SpyGlass DFT (Design for test at RTL)
- SpyGlass Power (Power analysis & reduction at RTL)
- SpyGlass CDC (Comprehensive clock domain crossing verification)
- SpyGlass Constraints (Complete design constraints management)
- SpyGlass Physical (Modeling of physical implementation at RTL)
- GenSys Assembly (High-level automated chip assembly)
- BugScope (Dynamic functional verification at RTL)

Employees
Over 300 worldwide; 75% in research and development

Growth
Eight consecutive years of revenue growth

Customers
200+ customers, including the top semiconductor and consumer electronics companies

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Locations
Direct sales & support offices in the US, France, Germany, UK, Japan, Taiwan, India and Israel. Distributors in China and Korea